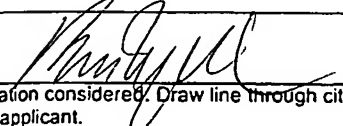


Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 01997-254002	Application No. 09/377,372
	Applicant Arvind Mithal et al.		
	Filing Date August 19, 1999	Group Art Unit 2731 2661	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
<i>BN</i>	AB	*EP 0329233	10/02/89	European Patent Office				
<i>BN</i>	AC	*EP 0 82902120 A2	09/12/97	European Patent Office				
	AD							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
<i>BN</i>	AE	Arvind et al., "Using Term Rewriting Systems to Design and Verify Processors", IEEE Micro Special Issue, May/June 1999
<i>BN</i>	AF	Babb et al., "Parallelizing Applications into Silicon", MIT Laboratory of Computer Science, April 1999
<i>BN</i>	AG	Cook et al., "Formal verification of explicitly parallel microprocessors", March 5, 1999
<i>BN</i>	AH	Gupta et al., "Hardware-software Co-synthesis for Digital Systems", Computer Science Laboratory, Stanford University, 1993
<i>BN</i>	AI	Liao et al., "An Efficient Implementation of Reactivity for Modeling Hardware in the Design Environment", University of California at Irvine, 1997
<i>BN</i>	AJ	Mathews et al., "Microprocessor Specification in Hawk", 1998
<i>BN</i>	AK	Poyneer et al., "A TRS Model for a Modern Microprocessor", MIT Computation Structures Group Memo 408, June 25, 1998
<i>BN</i>	AL	Shen et al., "Design and Verification of Speculative Processors", MIT Computations Structures Group Memo 400 (B), 1998
<i>BN</i>	AM	Shen et al., "Modeling and Verification of ISA Implementations", MIT Computations Structures Group Memo 400 (A), 1998
<i>BN</i>	AN	Shen et al., "Using Term Rewriting Systems to Design and Verify Processors" Massachusetts Institute of Technology (June 1999)
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<i>BN</i>	AP	Windley, P., "Verifying Pipelined Microprocessors", Brigham Young University, 1995
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Examiner Signature 	Date Considered 11/15/04
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	